

Edem, which fails to teach or suggest an isochronous channel including a linked list of buffers, as recited in the present claims. At col. 7, lines 15-54, Edem describes the use of buffers within an isochronous channel of a computer system. Nowhere, however, are buffers 132a or 132b described as being a linked list of buffers as claimed. Instead,  
5 it appears that buffers 132a and 132b may be page buffers. For example, Edem discloses that data from a first frame may be stored in a first buffer (e.g., buffer 132a) during a first frame period and, during a next frame period, data from the next frame may be stored in a second buffer (e.g., buffer 132b). Edem at col. 7, lines 25-29. There is no teaching or suggestion that buffers 132a and/or 132b may be implemented  
10 as a linked list of buffers as claimed. Accordingly, the present claims are fully patentable over Edem.

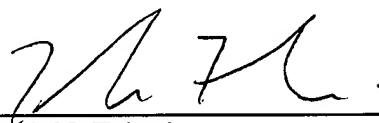
The objection to the title of the present application has been obviated by appropriate amendment. Formal drawings will be submitted upon receipt of a Notice of Allowance.

15 If there are any additional fees, please charge our deposit account no. 02-2666.

Respectfully submitted,

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